

Sir:

Transmitted herewith for filing is the Patent Application of:

Inventor: SHI-CHI LIN

For: METHOD OF MANUFACTURING DIELECTRIC ISOLATED SILICON STRUCTURE

16424 U.S. PTO
10/650601
06/26/03

Enclosed are:

- ☒ 3 sheets of drawing(s) - formal.
- ☒ An assignment of the invention to Taiwan Semiconductor Manufacturing Co.
- ☐ An associate power of attorney ☐ Applicant claims small entity status
- ☐ Request & Certification under 35 USC 122(b)(2)(b)(i)

The filing fee has been calculated as shown below:

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BASIC FEE				\$ 750.
TOTAL CLAIMS	20 -20=	0	x 18 =	\$ 0.
INDEP CLAIMS	3 -3=	0	x 84 =	\$ 0.
SUB TOTAL				\$ 750.
ASSIGNMENT				\$40.
TOTAL				\$ 790.

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- ☒ Any additional filing fees required under 37 CFR §1.16.
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Respectfully submitted,

STEPHEN S. ACKERMAN, REG. NO. 37,761

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I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail in an envelope addressed to Assistant Commissioner of Patents, Alexandria, VA, 22313-1450. Applicant and/or Attorney requests the date of deposit as the Filing Date.

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Application Number	
Filing Date	
First Named Inventor	
Art Unit	
Examiner Name	
Attorney Docket Number	TS01-999

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I am the :

- ☐ Applicant/Inventor.
- ☐ Assignee of record of the entire interest.
 Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96).
- ☒ Attorney or Agent of record.
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August 28, 2003

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